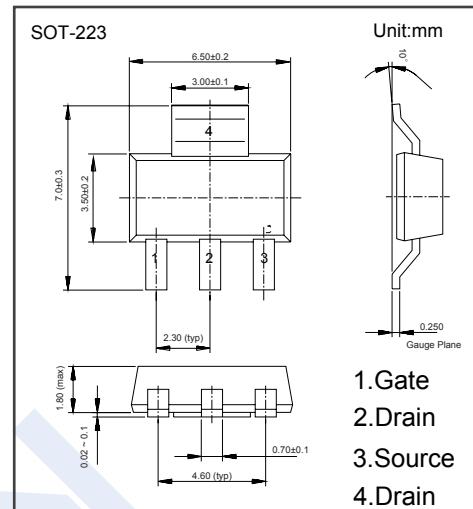
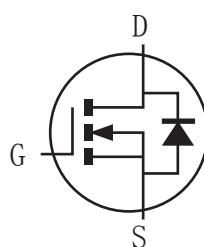


N-Channel MOSFET

ZXMN10A08G (KXMN10A08G)

■ Features

- $V_{DS} (V) = 100V$
- $I_D = 2.9 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 250m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 300m\Omega (V_{GS} = 6V)$



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current @ $V_{GS} = 10V$	I_D	2.9	A
		2.3	
		2	
Pulsed Drain Current	I_{DM}	11	
Power Dissipation	P_D	2	W
		3.9	$mW/^\circ C$
Linear derating factor		16	W
Linear derating factor		31	$mW/^\circ C$
Thermal Resistance.Junction- to-Ambient	R_{thJA}	62.5	$^\circ C/W$
		32	
Junction Temperature	T_J	150	$^\circ C$
Storage Temperature Range	T_{stg}	-55 to 150	

Note.1: For a device surface mounted on FR4 PCB measured at $t \leq 10$ sec.

Note.2: For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz in still air conditions.

N-Channel MOSFET

ZXMN10A08G (KXMN10A08G)

■ Electrical Characteristics $T_a = 25^\circ C$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=250 \mu A, V_{GS}=0V$	100			V
Zero Gate Voltage Drain Current	$I_{DS(on)}$	$V_{DS}=100V, V_{GS}=0V$			0.5	μA
Gate-Body Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250 \mu A$	2		4	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=3.2A$ (Note.1)			250	$m\Omega$
		$V_{GS}=6V, I_D=2.6A$ (Note.1)			300	
Forward Transconductance	g_{FS}	$V_{DS}=15V, I_D=3.2A$ (Note.1)		5		S
Input Capacitance	C_{iss}	$V_{GS}=0V, V_{DS}=50V, f=1MHz$		405		pF
Output Capacitance	C_{oss}			28.2		
Reverse Transfer Capacitance	C_{rss}			14.2		
Total Gate Charge	Q_g	$V_{GS}=5V, V_{DS}=50V, I_D=1.2A$		4.2		nC
Gate Source Charge	Q_{gs}	$V_{GS}=10V, V_{DS}=50V, I_D=1.2A$		7.7		
Gate Drain Charge	Q_{gd}			1.8		
Turn-On Delay Time	$t_{d(on)}$			2.1		
Turn-On Rise Time	t_r	$V_{GS}=10V, V_{DS}=30V, I_D=1.2A, R_G=6\Omega$		3.4		ns
Turn-Off Delay Time	$t_{d(off)}$			2.2		
Turn-Off Fall Time	t_f			8		
Body Diode Reverse Recovery Time	t_{rr}			3.2		
Body Diode Reverse Recovery Charge	Q_{rr}	$ I_F =1.2A, dI/dt=100A/\mu s, T_j=25^\circ C$		27		nC
Maximum Body-Diode Continuous Current	I_S			32		
Pulsed source current	I_{SM}				5	A
Diode Forward Voltage	V_{SD}	$I_S=3.2A, V_{GS}=0V, T_j=25^\circ C$			11	
					0.95	V

Note.1: Measured under pulsed conditions. Pulse width $\leq 300\mu s$; duty cycle $\leq 2\%$.

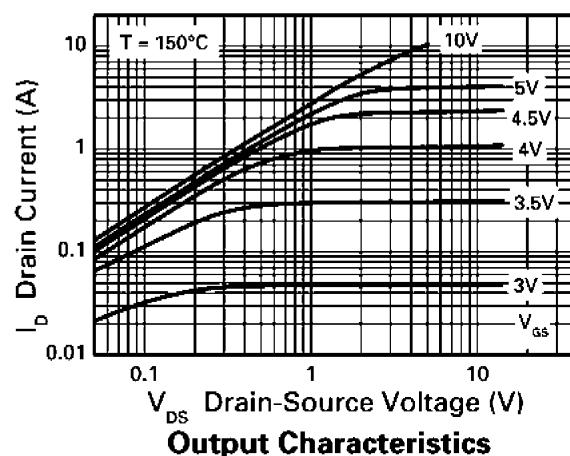
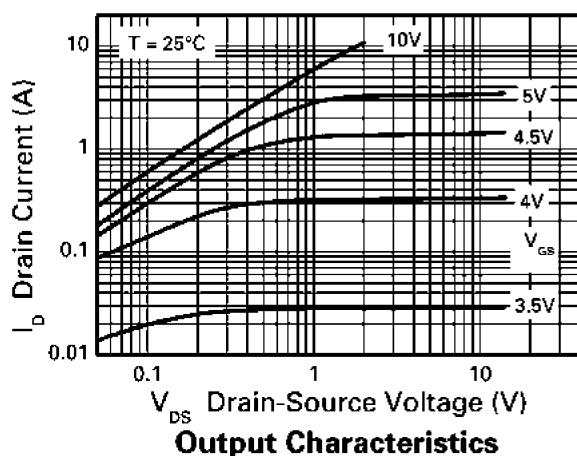
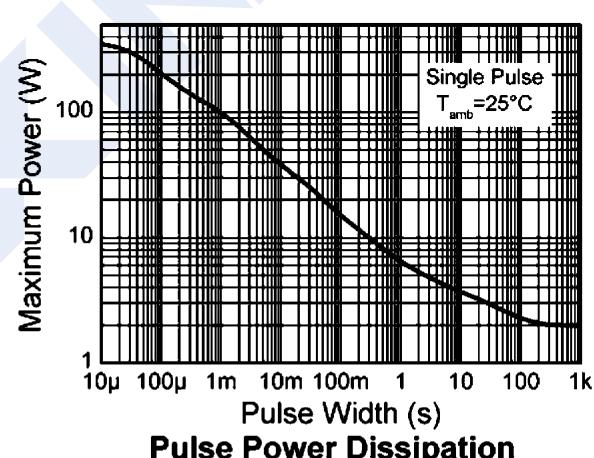
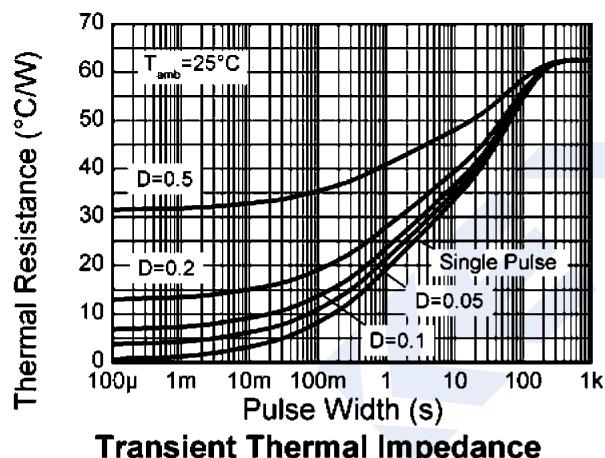
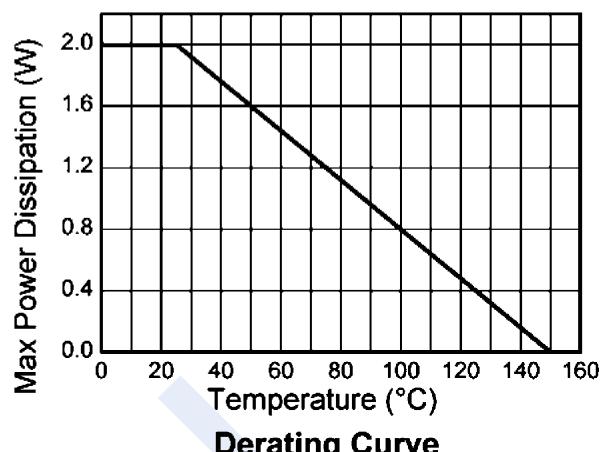
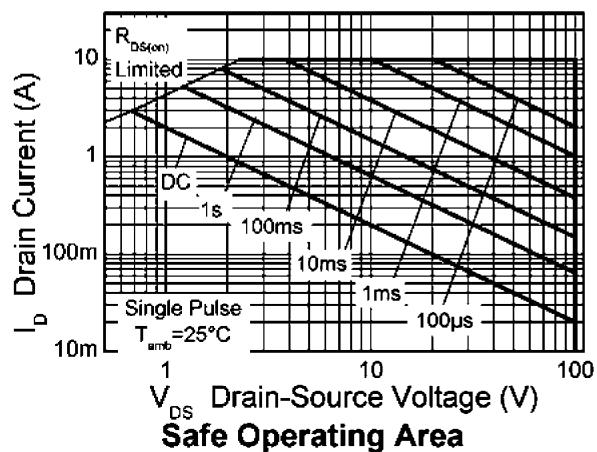
■ Marking

Marking	ZXMN 10A08
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N-Channel MOSFET

ZXMN10A08G (KXMN10A08G)

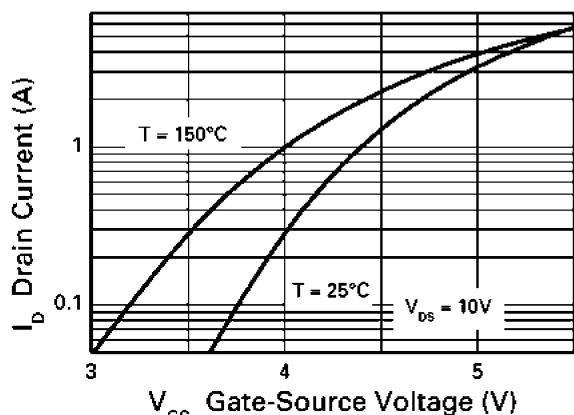
■ Typical Characteristics



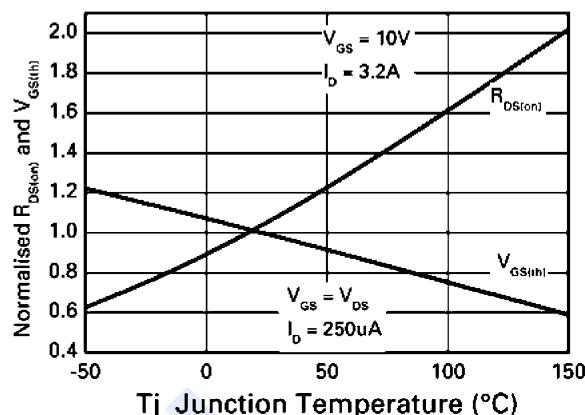
N-Channel MOSFET

ZXMN10A08G (KXMN10A08G)

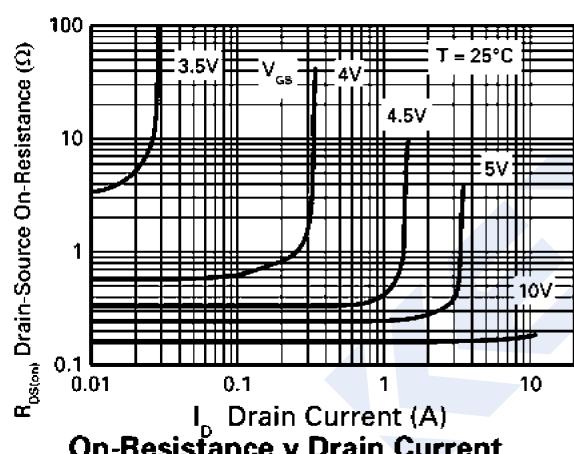
■ Typical Characteristics



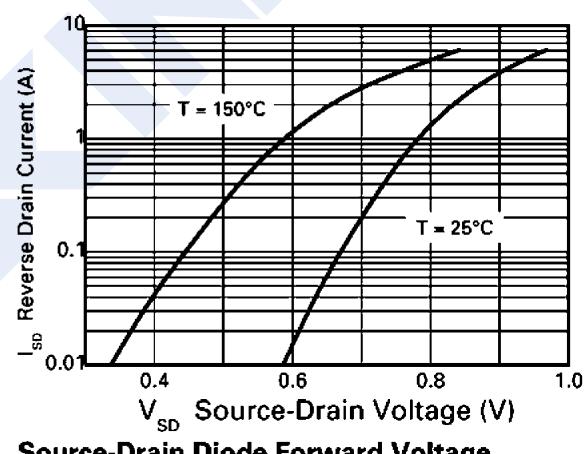
Typical Transfer Characteristics



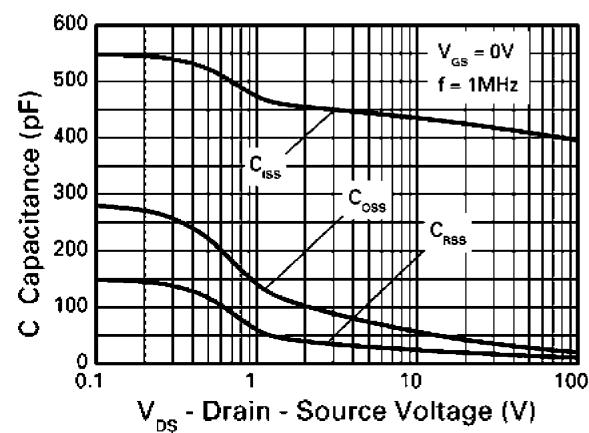
Normalised Curves v Temperature



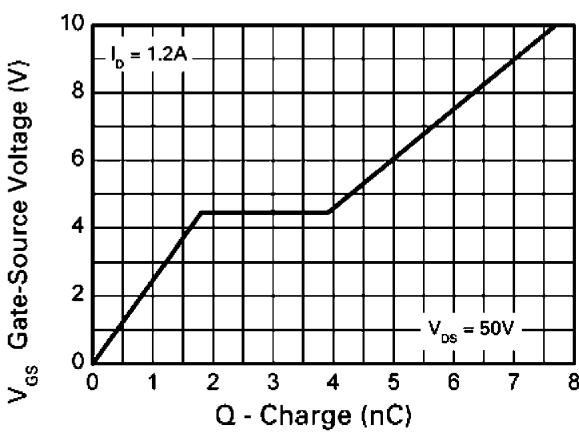
On-Resistance v Drain Current



Source-Drain Diode Forward Voltage



Capacitance v Drain-Source Voltage

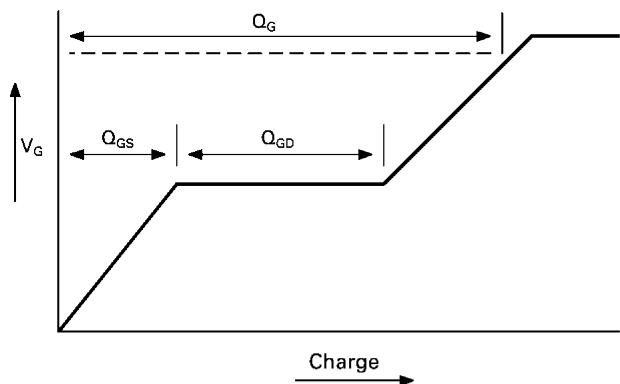


Gate-Source Voltage v Gate Charge

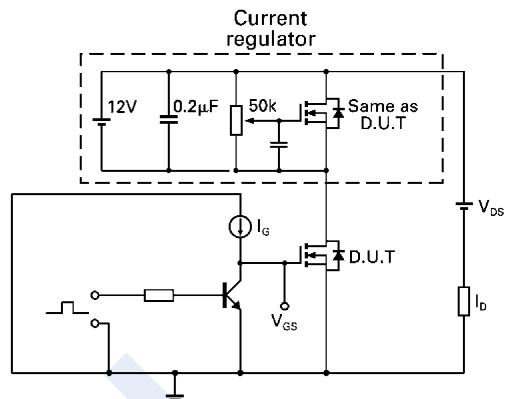
N-Channel MOSFET

ZXMN10A08G (KXMN10A08G)

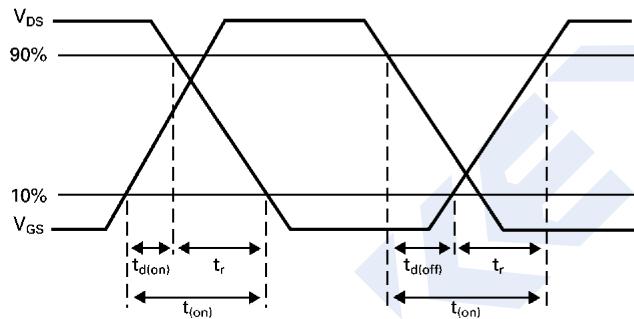
■ Typical Characteristics



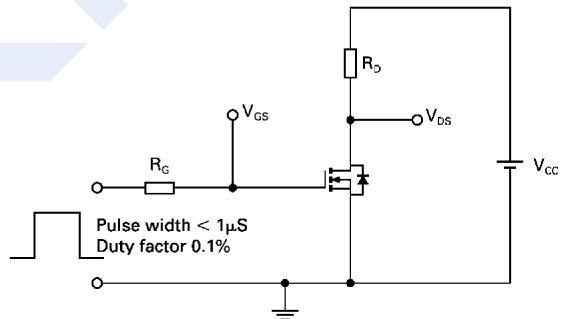
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms



Switching time test circuit